

## CLAIMS

1. A method of constructing an active matrix pixel device comprising:
  - providing a universal active matrix comprising on a substrate a matrix
  - 5 array of switching elements whose spacing defines a base pitch and sets of row address conductors and column address conductors for addressing the switching elements;
  - forming on the substrate a dielectric layer over the array of switching elements,
  - 10 - forming an array of contact holes in the dielectric layer such that contact can be made with a plurality of switching elements,
  - forming a pixel array on the universal active matrix, the pixel array comprising a matrix array of pixel electrodes in electrical contact with underlying switching elements via the contact holes, the spacing of the
  - 15 pixel electrodes defining a pixel pitch,wherein the pixel pitch is greater than the base pitch.
2. The method of Claim 1, wherein the array of contact holes is formed such that only a selected proportion of the switching elements are connected
- 20 to pixel electrodes.
3. The method of Claims 1 or 2, wherein the array of contact holes is formed such that at least some of the pixel electrodes are each in electrical contact with only one switching element.
- 25 4. The method of Claims 1, 2 or 3, wherein the pixel array is formed such that the pixel pitch is an integer multiple of the base pitch.
5. An active matrix pixel device comprising:
  - 30 - a universal active matrix comprising a matrix array of switching elements whose spacing defines a base pitch; and,

- a pixel array comprising a matrix array of pixel electrodes whose spacing defines a pixel pitch,  
wherein the pixel pitch is greater than the base pitch.

5     6.     The device of Claim 5, wherein a dielectric layer separates the universal active matrix from the pixel array.

7.     The device of Claim 6, wherein the dielectric layer comprises contact holes to allow each pixel electrode to contact with at least one underlying  
10     switching element.

8.     The device of Claims 5, 6 or 7, wherein only a proportion of the switching elements are connected to pixel electrodes.

15     9.     The device of any of Claims 5 to 8, wherein at least some of the pixel electrodes are each in electrical contact with only one switching element.

10.     The device of any of Claims 5 to 9, wherein the pixel pitch is an integer multiple of the base pitch.

20     11.     A liquid crystal display device comprising an active matrix pixel device of any of Claims 5 to 10.

12.     The display device of Claim 11, wherein the display is a reflective or  
25     transfective display device.

13.     The device of any of Claims 5 to 12, wherein the switching elements comprise thin film transistors.